

**Amendments to the Claims:**

This listing of claims will replace all prior versions and listings of claims in the Application:

**Listing of Claims:**

1. (currently amended) A liquid crystal display, comprising:
  - a first insulating substrate;
  - a gate line formed on the first insulating substrate;
  - a gate insulating layer formed on the gate line;
  - a data line formed on the gate insulating layer;
  - a passivation layer formed on the data line;
  - a pixel electrode formed on the passivation layer;
  - a second insulating substrate facing the first insulating substrate;
  - a common electrode formed on the second insulating substrate;
  - a first domain partitioning member formed on ~~at least one of the first and the second insulating substrate substrates~~; and,
  - a second domain partitioning member formed on ~~at least one of the first and the second insulating substrate substrates~~ and partitioning a pixel region into a plurality of domains along with the first domain partitioning member,
  - wherein ~~width each~~ of the domains has a width ~~[[is]]~~ equal to or less than 30 microns,
  - the pixel electrode has a chamfered edge, and
  - at least one of the first and second domain partitioning members has at least one oblique portion and at least one branch extending from the oblique portion.
2. (original) The liquid crystal display of claim 1, wherein the width of the domains is equal to or less than 28 microns.
3. (original) The liquid crystal display of claim 2, wherein the width of the domains is equal to or less than 22 microns.

4. (original) The liquid crystal display of claim 3, wherein the width of the domains is equal to or less than 17 microns.

5. (original) The liquid crystal display of claim 1, wherein the first domain partitioning member includes a cutout provided at the pixel electrode and the second domain partitioning member includes a cutout provided at the common electrode.

6. (original) The liquid crystal display of claim 5, wherein the width of the second domain partitioning member is equal to or less than 24 microns.

7. (original) The liquid crystal display of claim 6, wherein the width of the second domain partitioning member is equal to or less than 5 microns.

8. (currently amended) The liquid crystal display of claim 1, wherein an extension of the domains makes an angle of 45 degrees or 135 degrees with the gate line.

9. (original) The liquid crystal display of claim 1, wherein the data line has a triple-layered structure including an amorphous silicon layer, a doped amorphous silicon layer, and a metal layer.

10. (currently amended) A liquid crystal display, comprising:  
a first insulating substrate;  
a gate wire formed on the first insulating substrate and including a gate line, a gate electrode connected to the gate line, and a gate pad connected to the gate line;  
a storage electrode wire formed on the first insulating substrate and including a storage electrode line and a storage electrode branched from the storage electrode lines;  
a gate insulating layer formed on the gate wire and the storage electrode wire;  
an amorphous silicon layer formed on the gate insulating layer;  
a contact layer formed on the amorphous silicon layer;  
a data wire formed on the contact layer and including a data line intersecting the gate line, a data pad connected to the data line, a source electrode connected to the data line and lo-

cated adjacent to the gate electrode, and a drain electrode located opposite the source electrode with respect to the gate electrode;

a passivation layer formed on the data wire;

a pixel electrode formed on the passivation layer, connected to the drain electrode, and having a first cutout pattern;

a second insulating substrate facing the first insulating substrate;

a black matrix formed on the second insulating substrate layer and defining a pixel area;

a color filter formed on the pixel area; and,

a common electrode formed on the color filter and having a second cutout pattern,

wherein ~~width of~~ the second cutout pattern has a width ~~[[is]]~~ equal to or less than 24 microns,

the pixel electrode has a chamfered edge, and

at least one of the first and second cutout patterns has at least one oblique portion and at least one branch extending from the oblique portion.

11. (original) The liquid crystal display of claim 10, further comprising a liquid crystal layer interposed between the first insulating substrate and the second insulating substrate, wherein liquid crystal molecules included in the liquid crystal layer are aligned perpendicular to the first insulating substrate in absence of electric field.

12. (original) The liquid crystal display of claim 11, wherein the width of the second cutout pattern is equal to or less than 5 microns.

13. (original) The liquid crystal display of claim 11, wherein the width of the first and the second cutout patterns is equal to or less than cell gap of the liquid crystal layer.

14. (currently amended) The liquid crystal display of claim 11, wherein the first and the second cutout patterns partition a pixel region into a plurality of domains, each having a ~~and the~~ width ~~of the domains is~~ equal to or less than 28 microns.

App. Ser. No. 10/521,397  
Amendment dated Mar. 13, 2007  
Reply to Office action of Nov. 13, 2006

Docket No. AB-1408 US  
Ref. No. (OPP 043108 US)

15. (original) The liquid crystal display of claim 14, wherein the width of the domains is equal to or less than 22 microns.

16. (original) The liquid crystal display of claim 15, wherein the width of the domains is equal to or less than 17 microns.

17. (original) The liquid crystal display of claim 11, further comprising an overcoat interposed between the color filter and a common electrode.

18. (new) The liquid crystal display of claim 1, wherein the width of each of the domains is in the range of 9 to 30 microns.

19. (new) The liquid crystal display of claim 1, wherein the width of the second domain partitioning member is in the range of 9 to 15 microns.

20. (new) The liquid crystal display of claim 11, wherein the first and the second cutout patterns partition a pixel region into a plurality of domains, each having a width in the range of 9 to 30 microns.